**LM565/LM565C Phase Locked Loop**

**General Description**
The LM565 and LM565C are general purpose phase locked loops containing a stable, highly linear voltage controlled oscillator for low distortion FM demodulation, and a double balanced phase detector with good carrier suppression. The VCO frequency is set with an external resistor and capacitor, and a tuning range of 10:1 can be obtained with the same capacitor. The characteristics of the closed loop system—bandwidth, response speed, capture and pull in range—may be adjusted over a wide range with an external resistor and capacitor. The loop may be broken between the VCO and the phase detector for insertion of a digital frequency divider to obtain frequency multiplication.

The LM565H is specified for operation over the −55°C to +125°C military temperature range. The LM565CN is specified for operation over the 0°C to +70°C temperature range.

**Features**
- 200 ppm/°C frequency stability of the VCO
- Power supply range of ±5 to ±12 volts with 100 ppm/% typical
- 0.2% linearity of demodulated output
- Linear triangle wave with in phase zero crossings available
- TTL and DTL compatible phase detector input and square wave output
- Adjustable hold in range from ±1% to > ±60%

**Applications**
- Data and tape synchronization
- Modems
- FSK demodulation
- FM demodulation
- Frequency synthesizer
- Tone decoding
- Frequency multiplication and division
- SCA demodulators
- Telemetry receivers
- Signal regeneration
- Coherent demodulators

**Connection Diagrams**

**Metal Can Package**

**Dual-in-Line Package**

Order Number LM565H
See NS Package Number H10C

Order Number LM565CN
See NS Package Number N14A

© 1999 National Semiconductor Corporation www.national.com
### Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

- **Supply Voltage**: ±12V
- **Power Dissipation (Note 2)**: 1400 mW
- **Differential Input Voltage**: ±1V

### Operating Temperature Range

- LM565H: −55˚C to +125˚C
- LM565CN: 0˚C to +70˚C
- Storage Temperature Range: −65˚C to +150˚C
- Lead Temperature (Soldering, 10 sec.): 260˚C

### Electrical Characteristics

AC Test Circuit, $T_A = 25˚C$, $V_{CC} = ±6V$

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>LM565</th>
<th></th>
<th>LM565C</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Parameter</strong></td>
<td><strong>Conditions</strong></td>
<td><strong>Min</strong></td>
<td><strong>Typ</strong></td>
<td><strong>Max</strong></td>
<td><strong>Min</strong></td>
</tr>
<tr>
<td>Power Supply Current</td>
<td></td>
<td>8.0</td>
<td>12.5</td>
<td>8.0</td>
<td>12.5</td>
</tr>
<tr>
<td>Input Impedance (Pins 2, 3)</td>
<td>$-4V &lt; V_2, V_3 &lt; 0V$</td>
<td>7</td>
<td>10</td>
<td>5</td>
<td></td>
</tr>
<tr>
<td>VCO Maximum Operating Frequency</td>
<td>$C_o = 2.7 \text{ pF}$</td>
<td>300</td>
<td>500</td>
<td>250</td>
<td>500</td>
</tr>
<tr>
<td>VCO Free-Running Frequency</td>
<td>$C_o = 1.5 \text{ nF}$</td>
<td>$-10$</td>
<td>0</td>
<td>+10</td>
<td>$-30$</td>
</tr>
<tr>
<td>Operating Frequency Temperature Coefficient</td>
<td>$R_o = 20 \text{ k}\Omega$</td>
<td>100</td>
<td></td>
<td>$-200$</td>
<td></td>
</tr>
<tr>
<td>Frequency Drift with Supply Voltage</td>
<td></td>
<td>0.1</td>
<td>1.0</td>
<td>0.2</td>
<td>1.5</td>
</tr>
<tr>
<td>Triangle Wave Output Voltage</td>
<td></td>
<td>2</td>
<td>2.4</td>
<td>3</td>
<td>2</td>
</tr>
<tr>
<td>Triangle Wave Output Linearity</td>
<td></td>
<td>0.2</td>
<td></td>
<td>0.5</td>
<td></td>
</tr>
<tr>
<td>Square Wave Output Level</td>
<td></td>
<td>4.7</td>
<td>5.4</td>
<td>4.7</td>
<td>5.4</td>
</tr>
<tr>
<td>Output Impedance (Pin 4)</td>
<td></td>
<td>5</td>
<td></td>
<td>5</td>
<td></td>
</tr>
<tr>
<td>Square Wave Duty Cycle</td>
<td></td>
<td>45</td>
<td>50</td>
<td>55</td>
<td>40</td>
</tr>
<tr>
<td>Square Wave Rise Time</td>
<td></td>
<td>20</td>
<td></td>
<td>20</td>
<td></td>
</tr>
<tr>
<td>Square Wave Fall Time</td>
<td></td>
<td>50</td>
<td></td>
<td>50</td>
<td></td>
</tr>
<tr>
<td>Output Current Sink (Pin 4)</td>
<td></td>
<td>0.6</td>
<td>1</td>
<td>0.6</td>
<td>1</td>
</tr>
<tr>
<td>VCO Sensitivity</td>
<td>$f_o = 10 \text{ kHz}$</td>
<td>6600</td>
<td></td>
<td>6600</td>
<td></td>
</tr>
<tr>
<td>Demodulated Output Voltage (Pin 7)</td>
<td>±10% Frequency Deviation</td>
<td>250</td>
<td>300</td>
<td>400</td>
<td>200</td>
</tr>
<tr>
<td>Total Harmonic Distortion</td>
<td>±10% Frequency Deviation</td>
<td>0.2</td>
<td>0.75</td>
<td>0.2</td>
<td>1.5</td>
</tr>
<tr>
<td>Output Impedance (Pin 7)</td>
<td></td>
<td>3.5</td>
<td></td>
<td>3.5</td>
<td></td>
</tr>
<tr>
<td>DC Level (Pin 7)</td>
<td></td>
<td>4.25</td>
<td>4.5</td>
<td>4.75</td>
<td>4.0</td>
</tr>
<tr>
<td>Output Offset Voltage [V_2 − V_1]</td>
<td></td>
<td>30</td>
<td>100</td>
<td>50</td>
<td>200</td>
</tr>
<tr>
<td>Temperature Drift of [V_2 − V_1]</td>
<td></td>
<td>500</td>
<td></td>
<td>500</td>
<td></td>
</tr>
<tr>
<td>AM Rejection</td>
<td></td>
<td>30</td>
<td>40</td>
<td>40</td>
<td></td>
</tr>
<tr>
<td>Phase Detector Sensitivity $K_D$</td>
<td></td>
<td>0.68</td>
<td></td>
<td>0.68</td>
<td></td>
</tr>
</tbody>
</table>

**Note 1:** Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. Electrical Characteristics state DC and AC electrical specifications under particular test conditions which guarantee specific performance limits. This assumes that the device is within the Operating Ratings. Specifications are not guaranteed for parameters where no limit is given, however, the typical value is a good indication of device performance.

**Note 2:** The maximum junction temperature of the LM565 and LM565C is +150˚C. For operation at elevated temperatures, devices in the TO-5 package must be derated based on a thermal resistance of +150˚C/W junction to ambient or +45˚C/W junction to case. Thermal resistance of the dual-in-line package is +85˚C/W.
Typical Performance Characteristics

- Power Supply Current as a Function of Supply Voltage
- Lock Range as a Function of Input Voltage
- VCO Frequency
- Oscillator Output Waveforms
- Phase Shift vs Frequency
- VCO Frequency as a Function of Temperature
- Loop Gain vs Load Resistance
- Hold in Range as a Function of $R_{\text{c-7}}$
AC Test Circuit

Typical Applications

Note: S1 open for output offset voltage (V7 − V6) measurement.

2400 Hz Synchronous AM Demodulator

www.national.com
Typical Applications

FSK Demodulator (2025–2225 cps)

FSK Demodulator with DC Restoration
Typical Applications (Continued)

Frequency Multiplier (x10)

IRIG Channel 13 Demodulator
Applications Information

In designing with phase locked loops such as the LM565, the important parameters of interest are:

FREE RUNNING FREQUENCY

\[ f_0 \approx \frac{0.3}{R_0 C_0} \]

LOOP GAIN: relates the amount of phase change between the input signal and the VCO signal for a shift in input signal frequency (assuming the loop remains in lock). In servo theory, this is called the “velocity error coefficient.”

\[ \text{Loop gain} = K_0 K_0 \left( \frac{1}{\text{sec}} \right) \]

\[ K_0 = \text{oscillator sensitivity} \left( \frac{\text{radians/sec}}{\text{volt}} \right) \]

\[ K_0 = \text{phase detector sensitivity} \left( \frac{\text{volts}}{\text{radian}} \right) \]

The loop gain of the LM565 is dependent on supply voltage, and may be found from:

\[ f_0 = \frac{33.6 f_0}{V_C} \]

\[ f_0 = \text{VCO frequency in Hz} \]

\[ V_C = \text{total supply voltage to circuit} \]

Loop gain may be reduced by connecting a resistor between pins 6 and 7; this reduces the load impedance on the output amplifier and hence the loop gain.

HOLD IN RANGE: the range of frequencies that the loop will remain in lock after initially being locked.

\[ f_H = \frac{8 f_0}{V_C} \]

\[ f_H = \text{free running frequency of VCO} \]

\[ V_C = \text{total supply voltage to the circuit} \]

THE LOOP FILTER

In almost all applications, it will be desirable to filter the signal at the output of the phase detector (pin 7); this filter may take one of two forms:

A simple lag filter may be used for wide closed loop bandwidth applications such as modulation following where the frequency deviation of the carrier is fairly high (greater than 10%), or where wideband modulating signals must be followed.

The natural bandwidth of the closed loop response may be found from:

\[ f_n = \frac{1}{2\pi} \sqrt{\frac{K_0 K_0}{R_1 C_1}} \]

Associated with this is a damping factor:

\[ \delta = \frac{1}{2} \sqrt{\frac{1}{R_1 C_1 K_0 K_0}} \]

For narrow band applications where a narrow noise bandwidth is desired, such as applications involving tracking a slowly varying carrier, a lead lag filter should be used. In general, if \( 1/R_1 C_1 < K_0 K_0 \), the damping factor for the loop becomes quite small resulting in large overshoot and possible instability in the transient response of the loop. In this case, the natural frequency of the loop may be found from:

\[ f_n = \frac{1}{2\pi} \sqrt{\frac{K_0 K_0}{R_1 + R_2 C_1}} \]

\[ R_2 \] is selected to produce a desired damping factor \( \delta \), usually between 0.5 and 1.0. The damping factor is found from the approximation:

\[ \delta = 1 - \frac{1}{\pi} \sqrt{f_n} \]

These two equations are plotted for convenience.
Capacitor $C_2$ should be much smaller than $C_1$ since its function is to provide filtering of carrier. In general $C_2 \leq 0.1 \ C_1$. 

Damping Time Constant vs Natural Frequency
Physical Dimensions inches (millimeters) unless otherwise noted

Metal Can Package (H)
Order Number LM565H
NS Package Number H10C

Dual-In-Line Package (N)
Order Number LM565CN
NS Package Number N14A

www.national.com
LIFE SUPPORT POLICY

NATIONAL’S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT AND GENERAL COUNSEL OF NATIONAL SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

Notes

National does not assume any responsibility for use of any circuitry described, no circuit patent licenses are implied and National reserves the right at any time without notice to change said circuitry and specifications.