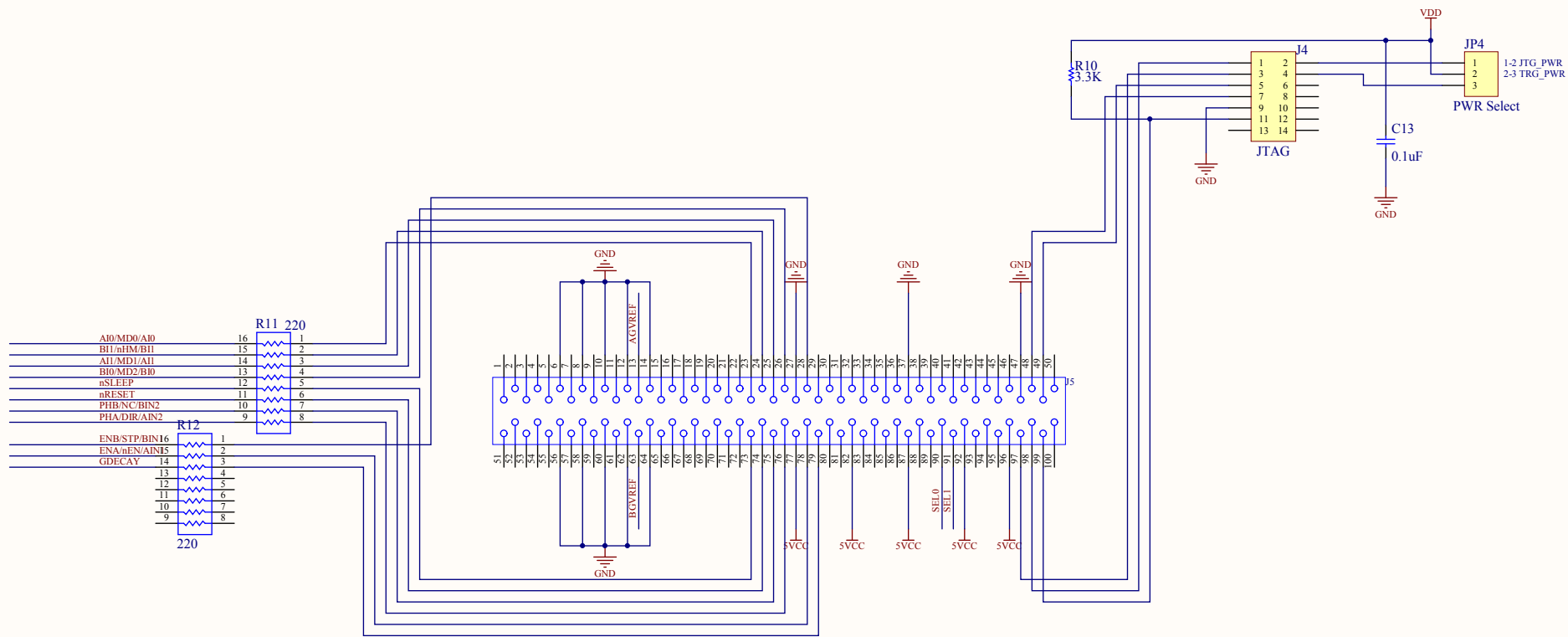
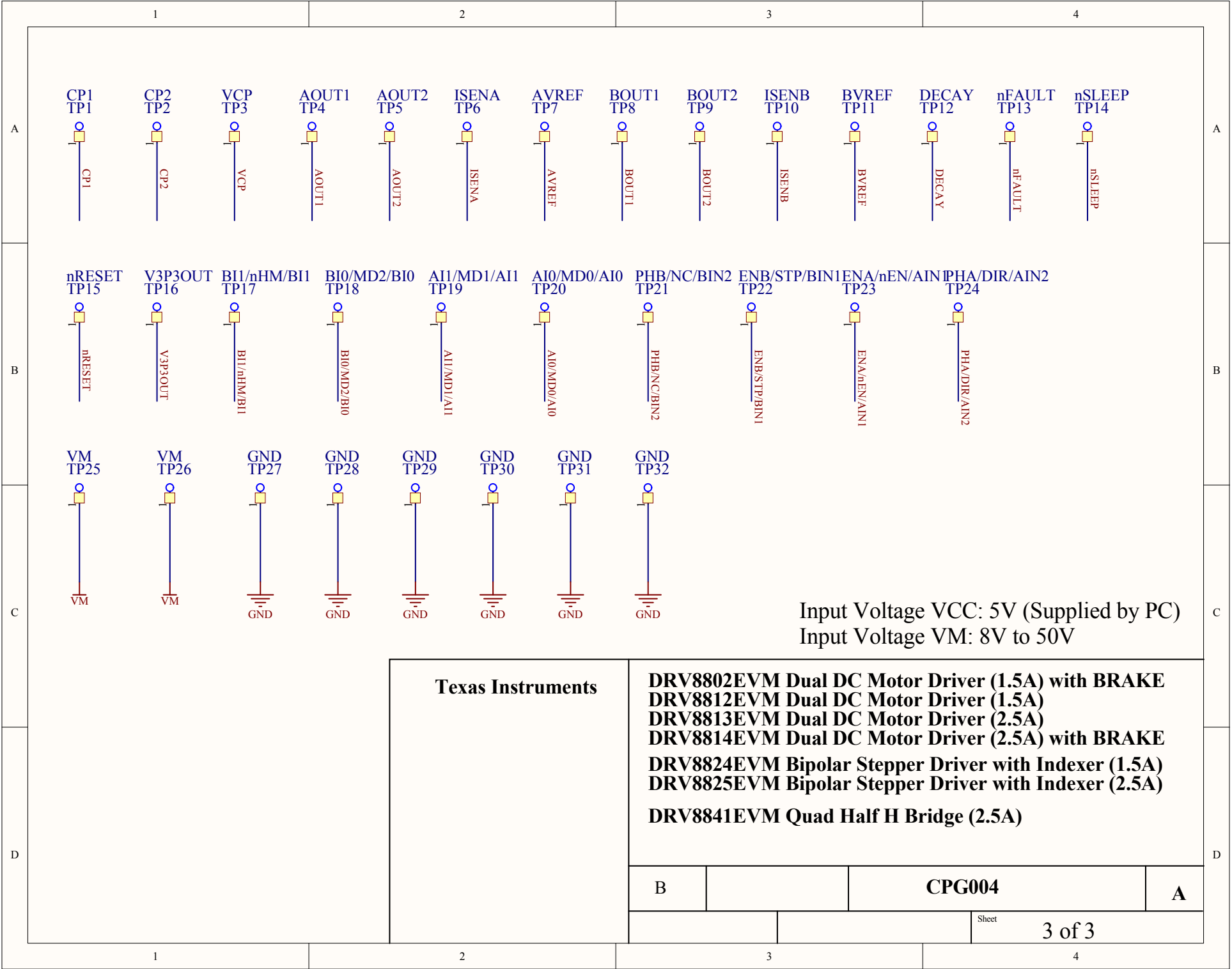
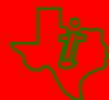






5.00

DRV8802/12/13/14/24/25/41
CPG004 REV D 4/25/2011



DRV88xx			
02/12 13/14	24/25	41	
BI1	nHM	BI1	
BI0	MD2	BI0	
AI1	MD1	AI1	
AI0	MD0	AI0	
PHB	NC	BIN2	
ENB	STP	BIN1	
ENA	nEN	AIN1	
PHA	DIR	AIN2	

WARNING! Device may get hot.

BUREF_SEL AUREF_SELECT

JP1:1-2 BUREf is Potentiometer
JP1:3-4 BUREf is MCU DAC Output
JP1:5-6 BUREf is same as AUREf
JP2:1-2 AUREf is MCU DAC Output
JP2:2 AUREf is Potentiometer

Remove AUREf

isconnected

MCU

DECAY

nFAULT

nSLEEP

nRESET

V3P3OUT

13 14

13 14

13 14

13 14

13 14

13 14

13 14

13 14

13 14

13 14

13 14

Layer Stack Up Detail for: CPG004 DRV88xxEVM

Layer Name

Top Overlay

Top Layer

MidLayer1 - KELVIN

BottomLayer