Updated ECE 410 Schedule
Spring 2008

Labs

Lab 6: Group lab with group/full lab report
Lab 7: Group lab with no report, only check off. Must begin working on Design Project while finishing Lab 7. Design project draft proposal will be turned in Monday after Lab 7.
Lab 8: Design project proposal; check off and final proposal. Must continue to work on Design Project while completing the proposal.
Lab 9: Design Project check off #1: Design phase. Should have all circuit schematics completed at this point.
Lab 10: Design Project check off #2: Layout and Simulation phase
Design Project Demonstrations

Homework

HW 7 postponed a week to allow teams more time to complete Lab 6.
HW 7 – 9 will be turned in weekly before Exam 2.

Schedule

Notice, to account for increased demand of your time for the design project and to allow more time for check offs, there will be no lecture on several Fridays.

3/10/08	Mon	Latches, Flip Flops, Tri-States, C2MOS	11.6-.7	
3/12/08	Wed	Counters, Logic Design Methods	11.8	
3/14/08	Fri	*Course/Lab Issues, Design Project Intro*		Lab6-G c/o
3/17/08	Mon	Adders	12.1-12.2	Lab6 grp-rpt
3/19/08	Wed	Adders	12.3-12.4	HW 7
3/21/08	Fri	Arithmetic Circuits, ALUs	12.5	Lab 7-G
3/24/08	Mon	Memory Basics, SRAM Cells	13.1	Lab 8 DP draft proposal
3/26/08	Wed	SRAM Arrays	13.2	HW 8
3/28/08	Fri	DP check, no lecture		Lab8 c/o: DP proposal
3/31/08	Mon	DRAM & other memories	13.3	Lab 8: DP proposal
4/2/08	Wed	Differential and Dynamic Logic	notes	HW 9
4/4/08	Fri	DP check, no lecture		Lab9 c/o: Design
4/7/08	Mon	Submicron Design	notes	
4/9/08	Wed	Exam Review & Examples	notes	
4/11/08	Fri	**Exam 2**		
4/14/08	Mon	exam review, design project issues	notes	
4/16/08	Wed	advanced VLSI projects; careers	notes	
4/18/08	Fri	DP check, no lecture		Lab10 c/o: Layout & Simulations
4/21/08	Mon	lab day, no lecture		DP demo
4/23/08	Wed	DP check, no lecture		DP demo
4/25/08	Fri	Engineering Design Day, no lecture		
4/30/08	Wed	Final project reports due. 12-noon		DP report